



PATENT

DOCKET NO. 005231/26-0051BS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Korbin S. Van Dyke, et al.

Serial No.: 09/666,110

Art Unit: 2183

Filed: August 20, 2000

Examiner: unassigned

Title: COMPUTER FOR EXECUTION OF TWO INSTRUCTION SETS

CERTIFICATE OF MAILING (37 C.F.R. § 1.8a)

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- Form PTO -1449

along with any paper(s) referred to as being attached or enclosed) and this Certificate of Mailing are being deposited with the United States Postal Service on date shown below with sufficient postage as first-class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Respectfully submitted,

SCHULTE ROTH & ZABEL

Dated: June 17, 2003

By: 

David E. Boundy

Registration No. 36,461

Mailing Address:

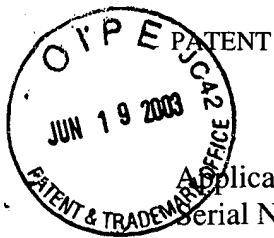
SCHULTE ROTH & ZABEL

919 Third Avenue

New York, New York 10022

(212) 756-2000

(212) 593-5955 Telecopier



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INFORMATION DISCLOSURE STATEMENT

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In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the items listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

The references listed on the enclosed Form 1449 are references that have come to light in applications listed in the Information Disclosure Statement of February 2001. Applicant has not reviewed all of them in detail. Of those that have been reviewed, none of the references are believed to be any more pertinent than the references provided in earlier IDS' and Forms 1449. However, in an abundance of caution, Applicant requests that they be considered.

For non-patent items listed on the enclosed Form PTO-1449 for which a copy is not already made of record in this application, a copy was previously cited by or submitted to the Patent and Trademark Office in application Serial No. 09/239,194, filed January 28, 1999, Yates et al., Executing Programs for a First Computer Architecture on a Computer of a Second Architecture, in application Serial No. 09/322,443, filed May 28, 1999, Reese et al., Profiling of Computer Programs Executing in Virtual Memory Systems, or in application Serial No. 09/385,394, filed August 30, 1999, Yates et al., Computer for Executing Two Different Instruction Sets, or in application 09/626,325, filed July 26, 2000, Yates, et al., Computer with Two Operating Systems.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on June 17, 2003 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

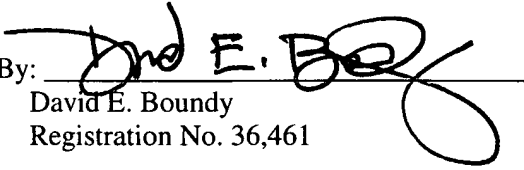
No fee is due for this Information Disclosure Statement since it is being filed in compliance with 37 C.F.R. §1.97(b)(3), to the knowledge of the undersigned, before the mailing date of a first Office Action on the merits.

The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account 50-0324, Order No. 005231/26-0051BS.

Respectfully submitted,

SCHULTE ROTH & ZABEL, LLP

Dated: June 17, 2003

By: 
David E. Boundy
Registration No. 36,461

CORRESPONDENCE ADDRESS:

SCHULTE ROTH & ZABEL, LLP
919 Third Avenue
New York, New York 10022
(212) 756-2000
(212) 593-5955 Facsimile